

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

Applicant:

Dmitriy Rumynin et al.

Title:

PARALLEL **LOGIC CIRCUIT** FOR **PERFORMING** COUNTER AND

MULTIPLICATION

Docket No.:

1365.051US1

Filed:

July 27, 2001

Examiner: Unknown

Commissioner for Patents

P.O.Box 1450

Alexandria, VA 22313-1450

Serial No.: 09/917,257

Due Date: N/A

Group Art Unit: 2121

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We are transmitting herewith the following attached items (as indicated with an "X"):

A return postcard. $\underline{\mathbf{X}}$

X An Supplemental Information Disclosure Statement (1 pg.), Form 1449 (1 pg.), and copies of 9 cited documents.

If not provided for in a separate paper filed herewith, Please consider this a PETITION FOR EXTENSION OF TIME for sufficient number of months to enter these papers and please charge any additional fees or credit overpayment to Deposit Account No. 19-0743.

SCHWEGMAN, LUNDBERG, WOESSNER & KLUTH, P.A.

P.O. Box 2938, Minneapolis, MN 55402 (612-373-6900)

Atty: Timothy B Reg. No. 40

CERTIFICATE UNDER 37 CFR 1.8: The undersigned hereby certifies that this correspondence is being deposited with the United States Postal Service with sufficient postage as first class mail, in an envelope addressed to: Commissioner for Patents, P.O.Box 1450, Alexandria, VA 22313-1450, on this 30 day of June, 2003.

Customer Number 21186

SCHWEGMAN, LUNDBERG, WOESSNER & KLUTH, P.A. P.O. Box 2938, Minneapolis, MN 55402 (612-373-6900) (GENERAL)

<u>S/N 09/917,257</u> <u>PATENT</u>

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

Applicant: Dmitriy Rumynin et al.

Examiner:

Unknown

Serial No.:

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Group Art Unit:

2121

Filed: Title: July 27, 2001

Docket:

1365.051US1

Title: A PARALLEL COUNTER AND A LOGIC CIRCUIT FOR PERFORMING

MULTIPLICATION
SUPPLEMENTAL INFORMATION DISCLOSURE STATEMENT RECEIVED

Assistant Commissioner for Patents P.O.Box 1450 Alexandria, VA 22313-1450

JUL 0.7 2003

Technology Center 2100

In compliance with the duty imposed by 37 C.F.R. § 1.56, and in accordance with 37 C.F.R. §§ 1.97 et. seq., the enclosed materials are brought to the attention of the Examiner for consideration in connection with the above-identified patent application. Applicants respectfully request that this Supplemental Information Disclosure Statement be entered and the documents listed on the attached Form 1449 be considered by the Examiner and made of record. Pursuant to the provisions of MPEP 609, Applicants request that a copy of the 1449 form, initialed as being considered by the Examiner, be returned to the Applicants with the next official communication.

Pursuant to 37 C.F.R. §1.97(b), it is believed that no fee or statement is required with the Supplemental Information Disclosure Statement. However, if an Office Action on the merits has been mailed, the Commissioner is hereby authorized to charge the required fees to Deposit Account No. 19-0743 in order to have this Supplemental Information Disclosure Statement considered.

The Examiner is invited to contact the Applicants' Representative at the below-listed telephone number if there are any questions regarding this communication.

Respectfully submitted,

DMITRIY RUMYNIN ET AL.

By their Representatives,

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Date 27 June 03

Timothy B Clise

Reg. No. 40,957

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Name

Signature

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US Patent & Tradement Office: U.S. DEPARTMENT OF COMMERCE
the Paperwork Reduction Act of 1995, no persons are required to respond to a collection of information unless it contains a valid OMB control number. Substitute for form 1449A/PTO Complete if Known INFORMATION DISCLOSURE **Application Number** 09/917,257 STATEMENT BY APPLICANT July 27, 2001 (Use as many sheets as necessary) Filing Date **First Named Inventor** Rumynin, Dmitriy RECEIVED Gr up Art Unit 2121 JUL 0 3 2003 & **Examiner Name** Unknown JUL 0 7 2003 Attorney Docket No: 1365.051US1 Sheet 1 of 1 Technology Center 2100

US PATENT DOCUMENTS									
Examiner Initial *	USP Document Number	Publication Date	Name of Patentee or Applicant of cited Document	Class	Subclass	Filing Date If Appropriate			
	US-4,399,517	08/16/1983	Niehaus, Jeffrey A., et al.	364	784	03/19/1981			
	US-5,095,547	03/17/1992	Kerns, Carol S.	2	160	06/13/1991			
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	US-5,497,342	03/05/1996	Mou, et al.	364	786	11/09/1994			
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		FOREIGN PATENT	DOCUMENTS			
Examiner Initials*	Foreign Document No	Publication Date	Name of Patentee or Applicant of cited Document	Class	Subclass	T ²

	OTHE	R DOCUMENTS NON PATENT LITERATURE DOCUMENTS		
Examiner Initials*	Cite Include name of the author (in CAPITAL LETTERS), title of the article (when appropriate), title of the No 1 (book, magazine, journal, serial, symposium, catalog, etc.), date, page(s), volume-issue number(s publisher, city and/or country where published.			
		GOTO, et al., "A 54 x 54-b Regularly Structured Tree Multiplier", IEEE Journal of		
		Solid-State Circuits, Vol 27, No. 9, (Sept. 1992),1229-1236		
		HEKSTRA, et al., "A Fast Parallel Multiplier Architecture", IEEE International		
		Symposium on Circuits and Systems; Institute of Electrical and Electronic		
		Engineers, c1977-c1996, 20v. :ill. :28cm, (1992),2128-2131		